Using Quartus II with Verilog to program Cyclone IV FPGA

Project wizard to execute Verilog code

Step 1. Create Project using the Quartus Project Wizard.

1. Enter the name and the top-level entity of the project
2. Select the required Target Device/Board

Step 2. Go to **New** and take Verilog HDL file and write code in that file and save it with the same name with which top level entity is names.

Step 3. Save project files and run **Analysis & Synthesis** from tasks which is left located on left corner of wizard.

Step 4. Assign pinout with board pins from **Assignments->Pin Planner** or

**Assignments->Assignment Editor**  and refer datasheet for respective pinout configuration.

Step 5. Compile design from tasks and build the design. If any error occurs then it’ll be available in Messages which is located in downside of wizard.

Step 6. Start with Programmer which is second last bar. Select Hardware Setup with USB Blaster driver. Select .sof file of your project if it is not auto detected by the system and start programming of device.